Системные макро-переменные OPENCNCMACRO.
Single Block Control Word(#1502)

	Description
Bit 0	Single block inhibit. 0(default): single block enabled, 1: single block disabled. When single block stop is disabled, single block stop operation is not performed even if the single block switch(by MLC C bit) is set to ON.
Bit 1	(*not yet implement*) Completion of an auxiliary function. 0(default): to be awaited, 1: not to be wait. When a wait for the completion of auxiliary functions (M, S, and T functions) is not to be waited, program execution proceeds to the next block before completion of auxiliary functions. Also, distribution completion signal DEN is not output.

- When the power is turned on, the value of this variable is 0.
- When single block stop is disabled, single block stop operation is not performed even if the single block switch(by MLC C bit) is set to ON.